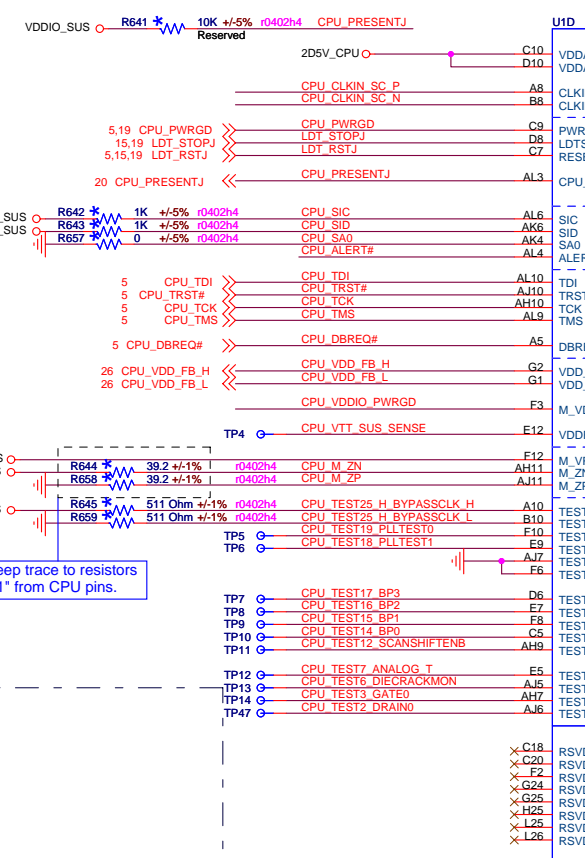
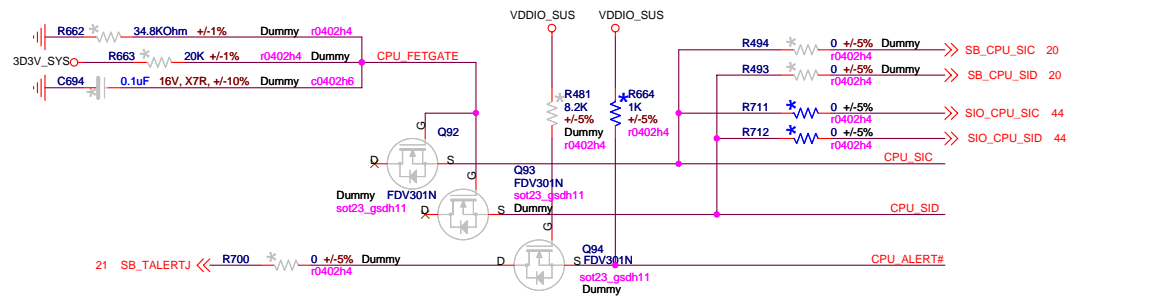
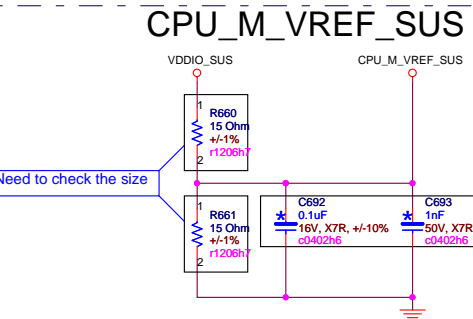
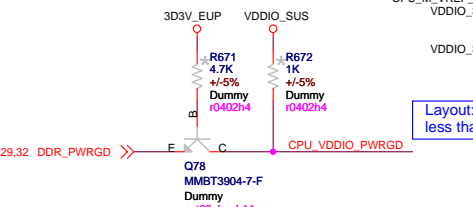
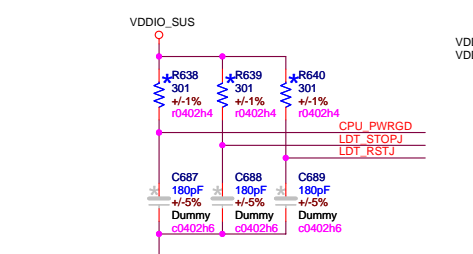
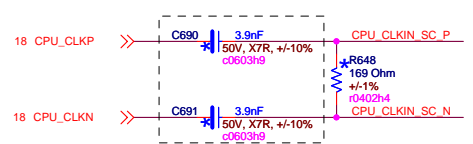
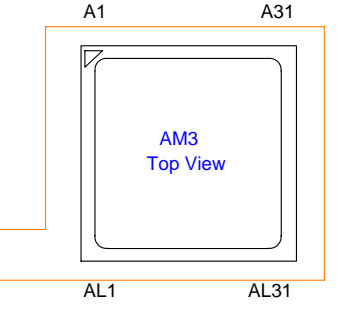
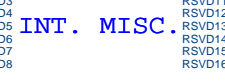
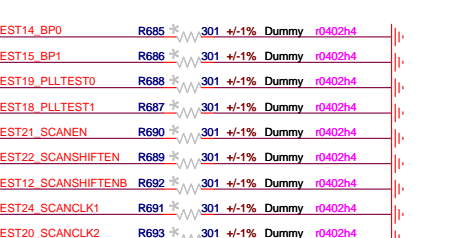
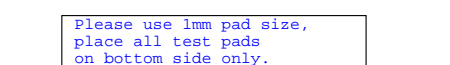
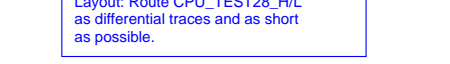
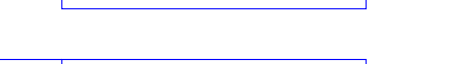
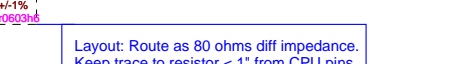
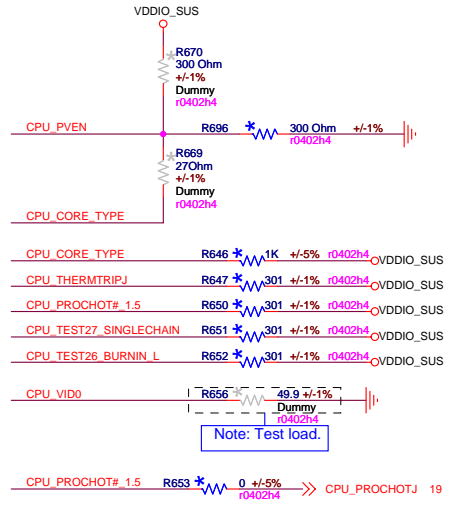
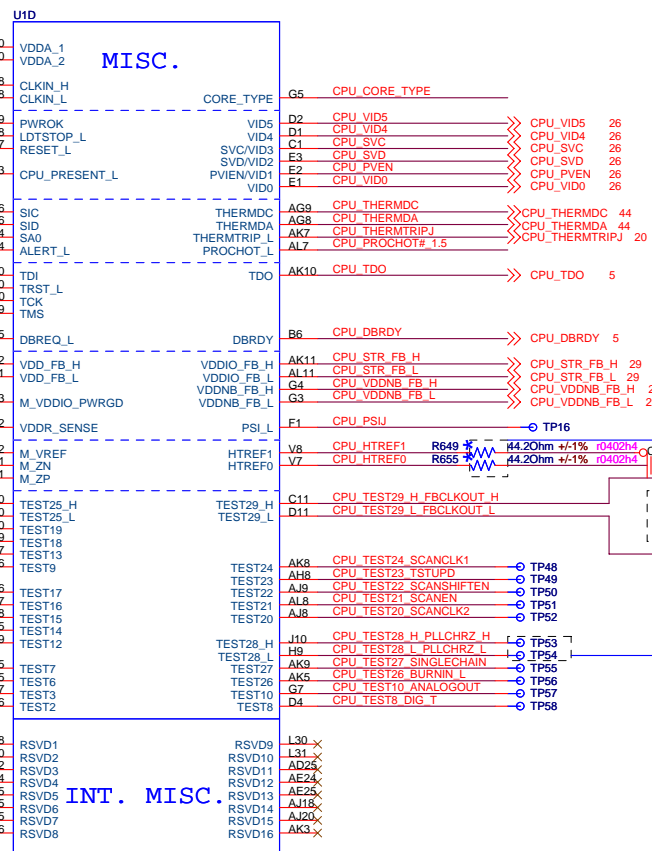


# CPU Control and Miscellaneous



Pin naming for VID pins indicate "Serial VID"/"Parallel VID" connections.



**FOXCONN**  
FOXCONN PCEG

Title: **AM3 CPU CONTROL & MISC**

Size: Custom Document Number: **RS88M05** Rev: A

Date: Wednesday, February 03, 2010 Sheet: 7 of 45